

PTO-1449 Information Disclosure Citation In an Application	Application No. 10/629,020		Applicant R. William Ezell	
	Docket Number 073671.0186	Group Art Unit 2631	Filing Date July 28, 2003	

U.S. PATENT DOCUMENTS

SL		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
SL	A	5,701,099	12/23/97	Shafir	327	552	11/27/95
SL	B	6,466,090 B1	10/15/02	Giuroiu	330	86	11/06/00
SL	C	2004/0100322 A1	05/27/04	Ganeshan et al.	327	553	11/25/02
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						
	M						
	N						
	O						
	P						

FOREIGN PATENT DOCUMENTS

SL		DOCUMENT NO.	DATE (month/day/year)	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
SL	Q	WO 96/27239	09/06/96	PCT	H03G	3/00		
	R							
	S							

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
T	Loh, et al., "A Versatile Digitally Controlled Continuous-Time Filter Structure with Wide-Range and Fine Resolutoin Capability," IEEE, Vol. 30, No. 5, pp. 265-276	May 1992
U	PCT search report for PCT/US2004/021579, 15 pages	Dec. 14, 2004
V		

EXAMINER

/Siu Lee/

DATE CONSIDERED

12/13/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:831801.1

PTO-1449	Application No. 10/629,020	Applicant(s) R. William Ezell	
	Docket Number 073671.0186	Group Art Unit 2631	Filing Date July 28, 2003

Information Disclosure Citation
in an Application

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
SL	A	US 6,304,135 B1	10/16/2001	John Matthew Muza	327	553	11/17/1999
SL	B	5,914,633	06/22/1999	Vittorio Comino et al.	327	553	08/08/1997
SL	C	6,112,125	08/29/2000	Randall L. Sandusky	700	28	03/08/1995
SL	D	5,352,934	10/04/1994	Sakhawat Khan	307	296.1	03/25/1993
SL	E	US 2003/0090316 A1	05/15/2003	Lutz Dathe et al.	327	554	06/27/2002
SL	F	5,245,646	09/14/1993	H. Spence Jackson et al.	377	2	06/01/1992
	G						
	H						
	I						
	J						
	K						
	L						
	M						
	N						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE (month/day/year)	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
SL	O	EP 1 172 933 A2	01/16/2002	Europe	H03H	11/04		XX
	P							
	Q							
	R							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	S	PCT/US2004/021750, Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration, 8 pgs	01/26/2005
	T		
	U		

EXAMINER /Siu Lee/	DATE CONSIDERED 12/13/2006
-----------------------	-------------------------------

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:839209.1

PTO-1449	Application No. 10/629,020	Applicant(s) R. William Ezell	
	Docket Number 073671.0186	Group Art Unit Unknown	Filing Date July 28, 2003



**Information Disclosure Citation
In an Application**

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A						
B						
C						
D						
E						
F						
G						
H						
I						
J						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
K							
L							
M							
N							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
SL	O	Geiger, et al., "VLSI Design Techniques for Analog and Digital Circuits," McGraw-Hill Publishing Company, pp. 651-656 plus title and copyright pages	1990
SL	P	Martinez, et al., "A 60-mW 200-MHz Continuous-Time Seventh-Order Linear Phase Filter With On-Chip Automatic Tuning System," IEEE Journal of Solid-State Circuits, Vol. 38; No. 2, pp. 216-225 plus cover page	02/2003
	Q		
	R		
	S		
	T		

EXAMINER

/Siu Lee/

DATE CONSIDERED

12/13/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:750447.1

PTO-1449		Application No.		Applicant(s) R. William Ezell	
Information Disclosure Citation In an Application		Docket Number 073671.0186	Group Art Unit	Filing Date July 28, 2003	

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	L							
	M							
	N							
	O							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
SL	P	Laker, et al., "Design of Analog Integrated Circuits and Systems," pp. 786-791 plus copyright page	1994
	Q		
	R		
	S		
	T		
	U		

EXAMINER <div style="text-align: center;">/Siu Lee/</div>	DATE CONSIDERED <div style="text-align: center;">12/13/2006</div>
--	--

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

DAL01:748622.1